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8.11.12

Experiment - 10

Object: WAP to arrange a block of data in ascending order.

Apparatus - Microprocessor Kit, DC Supply, Keyboard

Table :-

Address	Instructions	opcode	Description
2000	LXI H, 2000	21	Load Data of memory address
2001		00	4200 to H-L register pair
2002		42	
2003	MOV C, M	7E	Move data in memory to C
2004	DCR C	0D	Decrement by 1 in C
2005	MOV A, C	53	move data in C to register D
2006	LXI H, 2001	21	Load Data of memory
2007		01	address 4201 to H-L register
2008		42	pair
2009	MOV D, H	7F	Move data in memory to accumulator
200A	INX H	23	Increment by 1 in H
200B	CMP D	BE	Compare memory with accumulator
200C	JC 2004	DA	Jump to address 2004 if
200D		14	carry
200E		20	
200F	MOV B, M	46	Move data in memory to B
2010	MOV M, A	77	Move data in accumulator to memory
2011	DCR H	2B	Decrement by 1 in H
2012	MOV M, B	70	Move data in B to memory
2013	INX H	23	Increment by 1 in H

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Calculation :-

2009

(10)

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2014	DCR D	0B15	Decrement by 1 in D
2015	JNZ 2009	C2	Jump to address 2009 if no zero
2016		09	
2017		20	
2018	DCR C	0D	Decrement by 1 in C
2019	JNZ 2005	C2	Jump to address 2005 if no zero
201A		05	
201B		20	
201C	HLT	CF	End of Program.

Result:- The program to arrange a block of data in ascending order has been performed successfully.

Precaution:-

- Don't keep the power supply unnecessarily on.
- Type the opcodes carefully.

FL 25/4

D

right

accumula

accumulator  
if

try to B  
memory

memory  
at

8-12

2009	STA x D	12	Store accumulator value to D
200A	IN x H	23	Increment by 1 in H
200B	IN x D	13	Increment by 1 in D
200C	DCRB	05	Decrement by 1 in B
200D	JNZ 2008	C2	Jump when not zero
200E		03	
200F		20	
2010	HLT	CF	End of the program

Result:-

The transfer of 10000 has been successfully transferred

Precautions:-

- Switch the power supply when not in use
- Be careful while typing the opcodes since they cannot be erased and need to be overwritten from the beginning.

to accumulator  
 accumulator  
 200F  
 200E to B  
 200D to memory  
 200C  
 200B  
 200A  
 2009

(10)

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(7)

### Experiment 7

Object - To transfer a block of data from one memory location to another of 10 words.

#### Apparatus Required:

- 8085 microprocessor kit
- DC supply (5V)
- Keyboard

Address	Instruction	OP code	Description
2000	LXI H, 20F0h	21	load data of memory address 20F0 to register pair
2001		F0	
2002		20	
2003	LXI D, 20F0h	11	load data of 20F0 to DE register pair.
2004		00	
2005		2F	
2006	MVI B, 0A	06	Move data immediately to B.
2007		0A	
2008	MOV A, M	7E	Move data in memory to Accum

D  
register  
to accumulator  
accumulator  
20F0h  
hexy to B  
address to memory  
memory  
list.

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2008		20
2009	HLT	CF

End of program.

Result -

The 2's complement of a 8-bit number has been performed successfully.

Precautions -

Switch the power supply when not in use

Be careful while typing the opcode since they cannot be erased and need to be overwritten from the beginning.

*[Signature]*

address pair

to C

later D  
neg

7-2 right

copy to accumulator

with accumulator  
as 2014 if

memory to B

accumulator to memory

in H  
B to memory  
by list.

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Result:-

The 1's complement of a 8-bit number  
has been performed successfully

Precautions:-

1. Switch the power supply when not in use.
2. Be careful while typing the codes. Since they cannot be erased and need to be overwritten from the beginning.

~~N~~  
2/13

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### Experiment-5

objed:- WAP to obtain 2's complement of 8-bit number stored at 2050 location

#### Apparatus Required:

- 8085 microprocessor kit
- DC Supply (+5V)
- Keyboard

<u>Address</u>	<u>Instruction</u>	<u>op code</u>	<u>Description</u>
2000	LDA 2050	3A	load data in accumulator
2001		50	
2002		20	
2003	CMA	8F	Take is complement of Accumulator data.
2004	ADI, 01	C6	Store Add 1 in the Accumulator.
2005		01	
2006	STA 2051	32	Store result in accumulator at 2051
2007		51	

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2006	JNC 200A	D1	Jump if no carry generate
2007		0A	
2008		20	
2009	MOV A, M	7E	Move data from M to Register A
200A	STA 2603	32	Store data of accumulator at 2603
		03	
		26	
200B	HLT	CF	End of Program

~~Result:- Maximum out of two number has been performed successfully.~~

~~Precautions:-~~

- 1. Switch the power supply when not in use
- 2. Be careful while typing the opcodes. Since they cannot be erased and need to be overwritten from the beginning.

*MSB*



EXPERIMENT-7

Object:- write a program to perform 1's complement of a 8-bit number which store at 2050 location

Apparatus Required:-

- 8085 microprocessor kit
- DC supply (±5v)
- Keyboard

Address	Instruction	op code	Description
2000	LDA 2050	3A	load the data in accumulator
2001		50	
2002		20	
2003	CMA	8F	Takes 1's complement of Accumulator data
2004	STA 2051	32	Store result in accumulator at 2051
2005		51	
2006		20	
2007	HLT	CF	End of a program

## Experiment - 6

①

Object:- Write a program to find out maximum number between two numbers and display the maximum number.

### Apparatus Required:-

- 8085 microprocessor Kit
- DC Supply (+5V)
- Keyboard.

<u>Address</u>	<u>Instruction</u>	<u>opcode</u>	<u>Description</u>
2000	LXI H, 2601	21	Data store in H
2001		01	
2002		26	
2003	MOV A, H	7E	move data A to H Register.
2004	INX H	23	Increment in H it become for L
2005	CMP M	BE	Comparison is performed